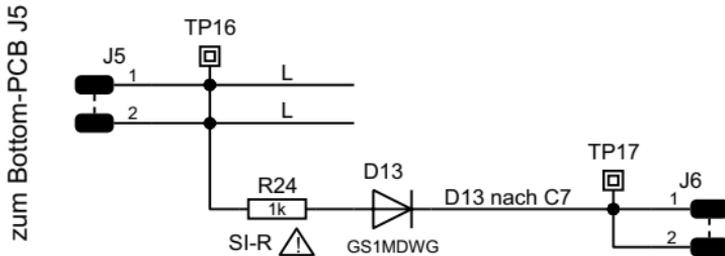
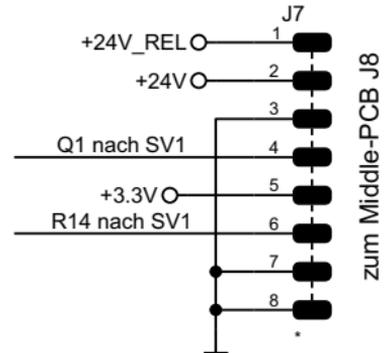
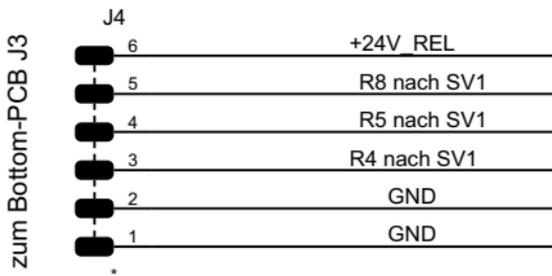
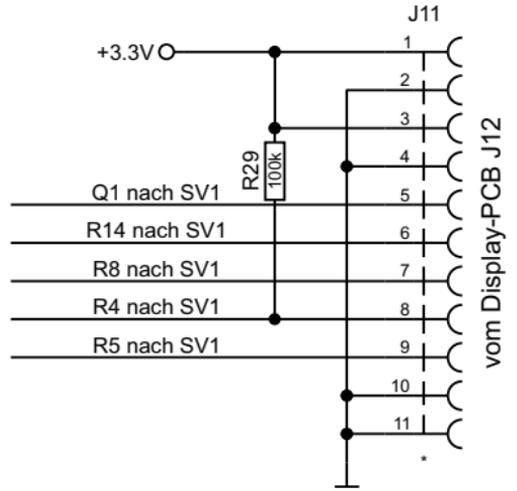
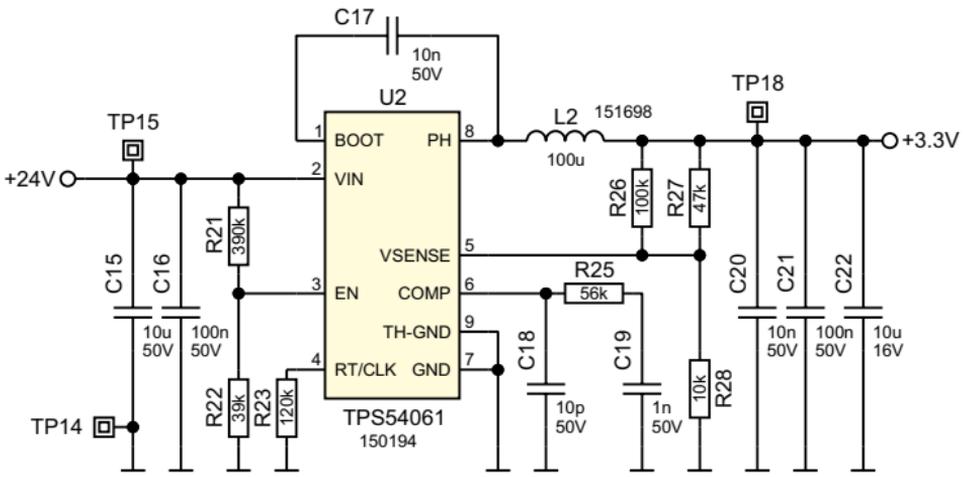


Bild 1: Schaltbild der Displayplatine (Display-PCB)



zum Middle-PCB J10

Bild 2: Schaltbild der Haupt-/Verbindungsplatine (Main-PCB)

