# SN5432, SN54LS32, SN54S32, SN7432, SN74LS32, SN74S32 QUADRUPLE 2-INPUT POSITIVE-OR GATES

DECEMBER 1983 - REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

### description

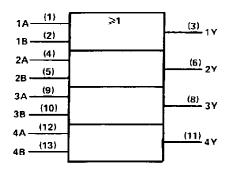
These devices contain four independent 2-input OR gates.

The SN5432, SN54LS32 and SN54S32 are characterized for operation over the full military range of  $-55\,^{\circ}\text{C}$  to  $125\,^{\circ}\text{C}$ . The SN7432, SN74LS32 and SN74S32 are characterized for operation from  $0\,^{\circ}\text{C}$  to  $70\,^{\circ}\text{C}$ .

#### **FUNCTION TABLE (each gate)**

INP	UTS	OUTPUT
Α	B	Y
Н	X	Н
Х	н	H
L	L	L

### logic symbol†



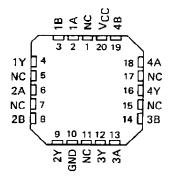
<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D. J. N. or W packages.

SN5432, SN54LS32, SN54S32 . . . J OR W PACKAGE SN7432 . . . N PACKAGE SN74LS32, SN74S32 . . . D OR N PACKAGE (TOP VIEW)

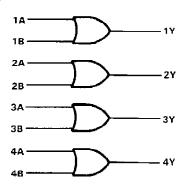
1A 🗆	1	U14 VCC
1B 🗀	2	13 <b>□ 4</b> B
1Y 🗆	3	12 🗆 4A
2A 🗌	4	11 47
2B 🗆	5	10 3B
2Y 🗖	6	9∐-3A
GND 🗖	7	8 <b>□</b> 3Y
	_	

SN54LS32, SN54S32 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

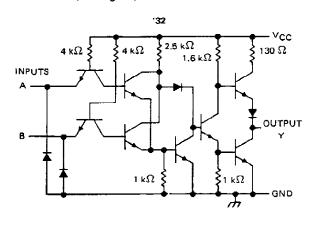
# logic diagram

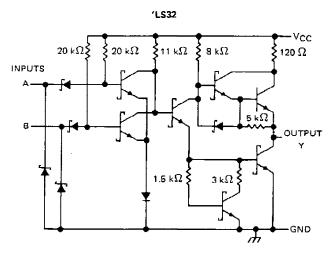


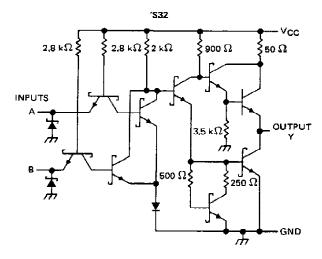
### positive logic

 $Y = A + B \text{ or } Y = \overline{\overline{A} \cdot \overline{B}}$ 

#### schematics (each gate)







Resistor values shown are nominal.

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	7 V
Input voltage: '32, 'S32	5.5 V
'L\$32	
Operating free-air temperature: SN54'	. –55°C to 125°C
SN74′	0°C to 70°C
Storage temperature range	65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

# recommended operating conditions

			SN5432			SN7432		UNIT
		MIN	NOM	MAX	MIN	NOM	0.8 - 0.8	ONT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
Уιн	Hgh-level input voltage	2			2			V
VIL	Low-level imput voltage			0.8			8,0	V
ЮН	High-level output current			- 0.8			<b>8.0</b> ~	mA
loL	Low-level output current			16			16	mΑ
ŤΑ	Operating free-air temperature	- 55	•	125	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

BABAMETER	TEST CONDITIONS 1	SN5432				UNIT	
PARAMETER	TEST CONDITIONS (	MIN TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	V <sub>CC</sub> = MIN, I <sub>I</sub> = - 12 mA		- 1.5			<b>— 1.5</b>	V
V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>IH</sub> ≈ 2 V, I <sub>OH</sub> ≠ − 0.8 mA	2.4 3.4		2.4	3.4		V
VOL	VCC = MIN, VIL = 0.8 V, IOL = 16 mA	0,2	0.4		0.2	0.4	V
lį	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V		1			1	mΑ
Чн	$V_{CC} = MAX$ , $V_1 = 2.4 V$		40			40	μA
liL.	$V_{CC} = MAX$ , $V_1 = 0.4 V$		<b>- 1.6</b>			- 1.6	mΑ
los§	V <sub>CC</sub> = MAX	- 20	<b>– 55</b>	- 18		- 55	mΑ
Іссн	V <sub>CC</sub> = MAX, See Note 2	15	22		15	22	mA
<sup>l</sup> CCL	VCC = MAX, VI = 0 V	23	38		23	38	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time.

NOTE 2: One input at 4.5 V, all others at GND.

# switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
tPLH I	A or B	>	B 400 O	C - 15 - 5		10	15	ns
†PHL	A 01 B	<u> </u>	$R_L = 400 \Omega$ ,	C <sub>L</sub> = 15 pF		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

# SN54LS32, SN74LS32 QUADRUPLE 2 INPUT POSITIVE OR GATES

### recommended operating conditions

		SN54LS32		SN74L	S32	UNIT
	MIN	NOM MA	х мі	NOM N	MAX	UNIT
V <sub>CC</sub> Supply voltage	4.5	5 5	.5 4.7	5 5	5.25	V
VIH Hgh-level input voltage	2			2		٧
VIL Low-level input voltage		C	.7		8.0	V
IOH High-level output current		- C	.4		D.4	mA
IOL Low-level output current			4		8	mA
TA Opertating free-air temperature	- 55	17	:5	0	70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS †			SN54LS	32		SN74LS	32	UNIT
PARAMETER				MIN	TYP\$	MAX	MIN	TYP ‡	MAX	UNII
V <sub>IK</sub>	V <sub>CC</sub> - MIN,	I <sub>1</sub> = 18 mA				- 1.5			- 1.5	V
Voн	VCC = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OH</sub> = - 0.4 mA	2,5	3.4	•	2,7	3.4		V
1.4	VCC = MIN,	VIL = MAX,	10L = 4 mA		0.25	0.4		0.25	0.4	v
VOL	V <sub>CC</sub> = MIN,	VIL = MAX,	ioL = 8 mA	i				0.35	0.35 0.5	
l <sub>l</sub>	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 7 V		1		0.1			0.1	mA
IH	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V			-	20			20	μΑ
IIL	VCC = MAX,	V1 = 0.4 V		ļ		0.4			- 0.4	mA
los §	VCC = MAX			- 20		<b>– 100</b>	<b>– 20</b>		<b>- 100</b>	mΑ
Іссн	V <sub>CC</sub> = MAX,	See Note 2			3.1	6.2	-	3.1	6.2	mA
ICCL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0 V			4.9	9.8		4.9	9.8	mΑ

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

# switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	TEST CONDITIONS				UNIT
tPLH	A or B	V	D - 21.0	C = 15 ==		14	22	пѕ
tPHL	AOLD	•	$R_{\perp} = 2 k\Omega$ ,	С∟ = 15 рғ		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second.

# recommended operating conditions

	······································		SN54S3	2		SN74S3	2	
		MIN	MOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
Уιн	High-level input voltage	2			2		-	V
VIL	Low-level input voltage			0.8			0.8	V
ІОН	High-level output current			1			<b>– 1</b>	mA
loL	Low-level output current			20			20	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDIT	PIONE T		SN54S32			SN74S3	2	UNIT
PARAWE ! ER		TEST CONDITIONS				MAX	MIN	TYP #	MAX	UNII
V <sub>IK</sub>	VCC = MIN,	lj = _ 18 mA				- 1.2			- 1.2	V
Voн	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	IOH = - 1 mA	2.5	3.4		2.7	3.4		V
VoL	V <sub>CC</sub> = MIN,	V <sub>IL</sub> = 0.8 V,	I <sub>OL</sub> = 20 mA			0.5			0.5	V
Ч	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 5.5 V				1		-	1	mA
ЧН	VCC = MAX,	V <sub>1</sub> = 2.7 V				50			50	μА
İIL	VCC = MAX,	V <sub>1</sub> = 0.5 V	·	_		- 2			<b>– 2</b>	mA
los§	V <sub>CC</sub> = MAX	-		- 40		<b>- 100</b>	- 40		<b>–</b> 100	mA
Гссн	V <sub>CC</sub> = MAX,	See Note 2			18	32		18	32	mA
ICCL	VCC = MAX,	V <sub>1</sub> = 0 V			38	68	1	38	68	mA

- † For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
- ‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. § Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second.
- NOTE 2: One input at 4.5 V, all others at GND.

# switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
tPLH .	АогВ	· · · · · · · · · · · · · · · · · · ·	D - 370 C	C <sub>1</sub> = 15 pF		4	7	ns
tPHL	AOFB	Y	R <sub>L</sub> = 280 Ω,			4	7	ns
tPLH	A or 8		B <sub>1</sub> = 280 O	C <sub>I</sub> = 50 pF		5		п\$
tPHL	A 01 8	·	R <sub>L</sub> = 280 Ω,	o[ 30 pi		5		ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

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